



 DEEPTech INNOVATION

STUDENT INNOVATION CHALLENGE

IESA DeepTech Hackathon 2026

AI-Enabled Chip Design • Embedded Development

Build breakthrough solutions using AI-enabled EDA workflows and development boards.
Shortlisted teams pitch directly to industry leadership.

REGISTRATION OPEN



15 Jan - 15 Feb

2026

GRAND FINALE EVENT



IESA Vision Summit '26

Bengaluru • Feb 25-26

Register Now →

PLATFORM OVERVIEW


About IESA DeepTech Hackathon

The IESA DeepTech Hackathon is a national platform designed to build practical, industry-ready solutions in two of the most critical technology domains shaping India's electronics future: semiconductor design and embedded systems.

"Designed to do more than just collect ideas — structured to help teams move from a problem statement to a clear solution architecture, working prototype, and a compelling pitch."

Two Future-Critical Tracks

 AI-Enabled Chip Design

 Embedded Development

Key Outcomes



Real Deep-Tech Innovation

Move beyond concepts to build functional prototypes supported by domain mentorship.



Grand Finale at Vision Summit

Top finalists pitch live to IESA leadership and industry experts in Bengaluru.

OFFICIAL STUDENT BROCHURE 2026



Industry Mentorship

Transparent evaluation framework and guidance from semiconductor veterans.

Why This Hackathon Matters

India's electronics and semiconductor ecosystem is entering a rapid growth phase. Industry demand is shifting from only coding to deep-tech building across silicon and embedded systems.

EDA Workflow Transformation

As chip design complexity increases, companies are urgently adopting AI/ML in EDA workflows to accelerate verification, optimize PPA, and improve design-space exploration.

Edge Computing Demand

Real-world products are being deployed at the edge, where secure, low-latency, and reliable embedded applications are becoming critical for industrial IoT and robotics.

What You Gain

Future-Critical Skills



Master AI + EDA and embedded/edge development two of the fastest-growing deep-tech skill areas.

Measurable Outcomes



Move beyond slides. Build working demos with benchmarks, results, and documentation.

Vision Summit Access



Unique opportunity for finalists to pitch on the main stage and network with ecosystem leaders.

OFFICIAL STUDENT BROCHURE 2026

Hackathon Timeline

🕒 Jan 16 - Feb 26, 2026




○ Phase 1: Ideation

○ Phase 2: Evaluation

○ Phase 3: Finale

Prizes & Benefits

 Total Prize Pool: ₹2,00,000



₹60,000

1ST RUNNER-UP

CHAMPION



₹1,00,000

GRAND WINNER

+ Vision Summit Stage Access



₹40,000

2ND RUNNER-UP

Beyond Cash Rewards



Vision Summit Pitch

Pitch live to IESA leadership & industry experts in Bengaluru.



Elite Networking

Connect with semiconductor leaders, startups & policymakers.



Career Opportunities

Visibility for internships & hiring with partner companies.



Sponsor Kits


Hardware kits, tool credits & goodies from partners.

ORGANIZED BY



India Electronics & Semiconductor Association

IESA is India's leading industry body for ESDM, semiconductors, and intelligent electronics. We connect industry, startups, academia, and policymakers to strengthen India's innovation and manufacturing ecosystem.

 iesaonline.org

HACKATHON PARTNER



i4C is a national innovation partner executing high-impact hackathons to validate ideas and build deployable solutions.

GRAND FINALE VENUE

IESA Vision Summit 2026

India's Flagship Semiconductor & ESDM Gathering

2026 THEME

"Design to Manufacturing — Synergy of Product, Production and Skill"



Feb 25–26, 2026

SAVE THE DATES



The Leela Bhartiya City

BENGALURU, INDIA



Event Scale & Opportunity

2000+

DELEGATES

60+

EXHIBITORS

100+

BOOTHS

20+

KEYNOTES

**Finalists will present their solutions directly to this massive ecosystem audience.*

Who Can Apply?



Students & Researchers

B.Tech, B.E., M.Tech, PhD scholars, and Research Fellows from recognized institutions.



Early Professionals – Interns (Register as students)

Interns with up to 1 or 2 years of industry experience.



Team Structure

Teams of 2–4 members recommended. Inter-disciplinary teams are encouraged.

? Frequently Asked Questions

>Is it online or offline?

Development phases are 100% online. The Grand Finale will be held physically at the IESA Vision Summit 2026 in Bengaluru.

>Do we need to buy hardware?

For Track 2 (Embedded), teams may use their own development boards (ESP32, STM32, RPi, etc.) for the prototype.

>What is the evaluation basis?

Innovation, technical feasibility, depth of solution, potential impact, and quality of the working demo.

>Can we modify the idea later?

Minor pivots are allowed during the mentoring phase, but the core problem statement must remain consistent.

What to Submit

 Pitch Deck

 Working Prototype / Demo

 Technical Documentation

 Code Repo / Video Link

**Detailed submission templates will be provided on landing page.*

Ready to Innovate?

Join the nation's premier deep-tech challenge. Build the future of electronics.

Register Now



Support Team



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Available Mon-Sat, 9 AM - 6 PM



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